

## **IN THE CLAIMS:**

21. (New) A computer chip configured to:

form a number of related data packets at a source device, wherein the related data packets represent a video signal;

form a first group of encrypted data packets by encrypting some of the data packets based upon a first set of encryption values, wherein the number of encrypted data packets in the first group of encrypted data packets is less than the number of data packets formed at the source device, and wherein the first group of encrypted data packets represents the video signal encoded at a first resolution;

form a second group of encrypted data packets by encrypting those data packets not already encrypted based upon a second set of encryption values, wherein each and every one of the related data packets is encrypted and belongs to either the first or the second group of encrypted data packets, and wherein the second group of encrypted data packets represents the video signal encoded at a second resolution;

transmit the encrypted data packets from the source device to a sink device coupled thereto;

decrypt the first group of encrypted data packets using a first set of decryption values corresponding to the first set of encryption values;

decrypt the second group of encrypted data packets using a second set of decryption values corresponding to the at least second set of encryption values concurrently with the decrypting of the first set of encrypted data packets; and

display the decrypted data packets by the sink device.